

## United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

| APPLICATION NO.        | . F    | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO.     | CONFIRMATION NO. |
|------------------------|--------|-------------|----------------------|-------------------------|------------------|
| 10/552,186             |        | 10/06/2005  | Chandler H. McIver   | 4905-A-13               | 6219             |
| 33136                  | 7590   | 07/05/2006  |                      | EXAMINER                |                  |
| WILLIAN                |        | ILL         | NGO, HUNG V          |                         |                  |
| 155 PARK<br>2141 E. HI |        | AVENUE      | ART UNIT             | PAPER NUMBER            |                  |
| PHOENIX                | AZ 850 | 16          | 2831                 |                         |                  |
|                        |        |             |                      | DATE MAILED: 07/05/2000 | S                |

Please find below and/or attached an Office communication concerning this application or proceeding.

|                  |   | Application No.                      | Applicant(s)                         | -  |
|------------------|---|--------------------------------------|--------------------------------------|--|
|                  |   | 10/552,186                           | MCIVER, CHANDLER H.                  |  |
|                  | Office Action Summary   | Examiner                             | Art Unit                             | <del>:                                    </del> |
|                  |   | Hung V. Ngo                          | 2831                                 |  |
| Period fo        | The MAILING DATE of this communication app<br>or Reply  | 1 ~ ~                                | ith the correspondence address       |  |
|                  | ORTENED STATUTORY PERIOD FOR REPLY  | VIS SET TO EXPIRE 3 M                | ONTH(S) OR THIRTY (30) DAYS          |  |
| WHIC             | CHEVER IS LONGER, FROM THE MAILING DA   | ATE OF THIS COMMUNI                  | CATION.                              | : : .  |
|                  | nsions of time may be available under the provisions of 37 CFR 1.13 SIX (6) MONTHS from the mailing date of this communication.   | 36(a). In no event, however, may a   | eply be timely filed                 |  |
| - Failu<br>Any i | period for reply is specified above, the maximum statutory period we<br>re to reply within the set or extended period for reply will, by statute,<br>reply received by the Office later than three months after the mailing | , cause the application to become Al | BANDONED (35 U.S.C. § 133).          |  |
|                  | ed patent term adjustment. See 37 CFR 1.704(b).   |                                      |                                      |  |
| Status           |   |                                      |                                      |  |
| ·                |   |                                      |                                      |  |
| ′=               | •—  | action is non-final.                 |                                      |  |
| 3)               | Since this application is in condition for allowar  | ·                                    |                                      |  |
|                  | closed in accordance with the practice under E  | x parte Quayle, 1935 C.D             | ). 11, 453 O.G. 213.                 | :  |
| Dispositi        | on of Claims  |                                      |                                      |  |
| <b>4</b> )⊠      | Claim(s) <u>1-9</u> is/are pending in the application.  |                                      |                                      |  |
| •                | 4a) Of the above claim(s) is/are withdray   | vn from consideration                |                                      |  |
|                  | Claim(s) is/are allowed.  |                                      |                                      |  |
| ·                | Claim(s) 1-9 is/are rejected.   |                                      |                                      |  |
| ·                | Claim(s) is/are objected to.  |                                      |                                      |  |
| · ·              | Claim(s) are subject to restriction and/or  | r election requirement.              |                                      |  |
|                  | _   |                                      |                                      |  |
| Applicati        | on Papers   |                                      |                                      |  |
| 9) 🔲 🤄           | The specification is objected to by the Examine   | r.                                   |                                      |  |
| 10) 🔲            | The drawing(s) filed on is/are: a)☐ acce  | epted or b) Dobjected to             | by the Examiner.                     |  |
|                  | Applicant may not request that any objection to the   | •                                    |                                      |  |
| 🗀 .              | Replacement drawing sheet(s) including the correcti   |                                      |                                      |  |
| 11)              | The oath or declaration is objected to by the Ex  | aminer. Note the attached            | d Office Action or form PTO-152.     |  |
| Priority u       | ınder 35 U.S.C. § 119   |                                      |                                      |  |
| 12) 🗆            | Acknowledgment is made of a claim for foreign   | priority under 35 U.S.C. 8           | 5 119(a)-(d) or (f)                  |  |
|                  | ☐ All b)☐ Some * c)☐ None of:   | priority unitable to c.c.c. s        | , 110(3) (4) 31 (1)                  |  |
| , -              | 1. Certified copies of the priority documents   | s have been received.                |                                      |  |
|                  | 2. Certified copies of the priority documents   |                                      | pplication No                        |  |
|                  | 3. Copies of the certified copies of the prior  | ity documents have been              | received in this National Stage      |  |
|                  | application from the International Bureau   | ı (PCT Rule 17.2(a)).                |                                      | i i .  |
| * S              | ee the attached detailed Office action for a list of  | of the certified copies not          | received.                            |  |
|                  |   |                                      |                                      |  |
|                  |   |                                      |                                      | : ! '  |
| Attach           | V-1   |                                      | 1000 大学 (101)                        |  |
| Attachment       | t(s) e of References Cited (PTO-892)  | 4) Thiopian S                        | Summary (PTO-413)                    |  |
| 2) Notice        | e of Draftsperson's Patent Drawing Review (PTO-948)   | Paper No(s                           | s)/Mail Date                         |  |
| 3) 🔯 Inform      | nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date <u>10-06-05</u> .  | 5)  Notice of II                     | nformal Patent Application (PTO-152) | . ! .  |
|                  |   |                                      |                                      |  |

Application/Control Number: 10/552,186

Art Unit: 2831

## **DETAILED ACTION**

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-4 are rejected under 35 U.S.C. 102(e) as being anticipated by Lo et al (US 6,507,120).

Re claim 1, Lo et al disclose a small outline leadess package comprising:

(a) a molding compound (224) encapsulating discrete

semiconductor devices (200) comprising one of a solder contact bump, small outline integrated circuit and a solder contact bump small outline transistor to form a package having a top, bottom and four side surfaces (Fig 2);

- (b) a pair of electrical contacts (202) each contacting a solder contact bump and extending to and flush one of said surfaces;
- (c) each of said electrical contacts providing an exposed contact surface coplanar with said one of said surfaces and terminating at a junction between said one of said surfaces and another of said surfaces (Fig 2); and
- (d) the termination of said contacts occurring only at oppositely disposed package surfaces (Fig 2).

Re claim 2, wherein said one of said surfaces is a top, bottom surface and other surface is a side surface (Fig 2)

Application/Control Number: 10/552,186

Art Unit: 2831

Re claim 3, wherein each of said electrical contacts include longitudinally extending notches (Fig 2).

Re claim 4 wherein the exposed contact surface of said electrical contacts is shaped to identify the electrical function of the encapsulated device (Fig 2).

Claims 1-5 are rejected under 35 U.S.C. 102(b) as being anticipated by Sunada (US 5,625,223)

Re claim 1, Sunada discloses a small outline leadess package comprising:

- (a) a molding compound (2') encapsulating discrete semiconductor devices (4') comprising one of a solder contact bump, small outline integrated circuit and a solder contact bump small outline transistor to form a package having a top, bottom and four side surfaces (Fig 5);
- (b) a pair of electrical contacts (3'a, 3'b) each contacting a solder contact bump and extending to and flush one of said surfaces;
- (c) each of said electrical contacts providing an exposed contact surface coplanar with said one of said surfaces and terminating at a junction between said one of said surfaces and another of said surfaces (Fig 5); and
- (d) the termination of said contacts occurring only at oppositely disposed package surfaces (Fig 5).

Re claim 2, wherein said one of said surfaces is a top, bottom surface and other surface is a side surface (Fig 5)

Re claim 3, wherein each of said electrical contacts include longitudinally extending notches (Fig 5).

Re claim 4 wherein the exposed contact surface of said electrical contacts is shaped to identify the electrical function of the encapsulated device (Fig 5).

Re claim 5, wherein one of said electrical contacts is S-shaped for contacting solder bumps on a surface of said discrete semiconductor device and wherein a second electrical contact is planar and contacts solder contact bumps on an opposite side of said discrete semiconductor device (Fig 5).

Claims 6, 7 are rejected under 35 U.S.C. 102(b) as being anticipated by Song et al (US 5,770,888)

Re claim 6, Song et al disclose a small outline leadless package comprising:

(a) a molding compound (26) encapsulating discrete semiconductor devices comprising one of a solder contact bump small outline integrated circuit solder contact bump small outline transistor to form a package having top, bottom and four side surfaces;

(b) a pair of S-shaped eclectically contacts (22b) each contacting

a solder contact bump (25) and extending to and flush with one of said surfaces, said eclectically contacts contacting solder bumps on a top surface of the encapsulated

(c) each of said electrical contacts providing an exposed contact surface coplanar with one of said package surfaces and terminating at a junction between said one of said package surfaces and another of said package surfaces (Fig 4); and

device and extending to a bottom surface of said package;

(d) the termination of said contacts occurring only at oppositely disposed package surfaces (Fig 4).

Application/Control Number: 10/552,186

Art Unit: 2831

Re claim 7, wherein said one of said surfaces is a bottom package surface and another of said surfaces is a side package surface (Fig 4).

Claim 8, 9 are rejected under 35 U.S.C. 102(b) as being anticipated by Cavasin (US 5,302,849)

Re claim 8, cavasin disclose a small outline leadless package comprising:

(a) a molding compound (32) encapsulated discrete semiconductor device (12)

comprising one of a solder contact bump small outline integrated circuit and a solder

contact bump small outline transistor to form a package having a top, bottom and four

side surfaces;

- b) a pair of L-shaped electrical contacts (18) each contacting a solder contact bump (17) and extending to and flush with two of said package surfaces;
- (c) said electrical contact providing an exposed contact surface coplanar with each of said two package surfaces and terminating at a junction between said two package surfaces (front page); and
- (d) the termination of said contacts occurring only at oppositely disposed package surfaces (front page).

Re claim 9, wherein said discrete semiconductor device is mounted between two vertically extending legs of opposing L-shaped electrical conductors (Front page)

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hung V. Ngo whose telephone number is (571) 272-1979. The examiner can normally be reached on Monday to Thursday 8:30-6:00.

Art Unit: 2831

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Dean A. Reichard can be reached on (571) 272-2800 EXT 31. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

HVN 06-26-06

> HUNG V. NGO PRIMARY EXAMINER